

1	SUPERCONDUCTOR (E.G., CRYOGENIC, ETC.)	37	MULTIFUNCTIONAL OR PROGRAMMABLE (E.G., UNIVERSAL, ETC.)
2	.Tunneling device	38	.Having details of setting or programming of interconnections or logic functions
3	..Josephson tunneling device		
4	...Plural devices (e.g., distributive device, etc.)		
5	...Interference device (i.e., SQUID)	39	.Array (e.g., PLA, PAL, PLD, etc.)
6	..Function of AND, OR, NAND, NOR, or NOT	40	..With flip-flop or sequential device
7	.Function of AND, OR, NAND, NOR, or NOT	41	..Significant integrated structure, layout, or layout interconnections
8	SECURITY (E.G., ACCESS OR COPY PREVENTION, ETC.)	42	..Bipolar transistor
9	RELIABILITY	43	...Emitter-coupled logic or emitter-follower logic
10	.Redundant		
11	..Voter circuit (e.g., majority logic, etc.)	44	..Field-effect transistor
12	..With flip-flop	45	...Complementary FET's
13	..With field-effect transistor	46	.Sequential (i.e., finite state machine) or with flip-flop
14	.Fail-safe	47	.Significant integrated structure, layout, or layout interconnections
15	.Parasitic prevention in integrated circuit structure	48	.Bipolar transistor
16	WITH TEST FACILITATING FEATURE	49	.Field-effect transistor
17	ACCELERATING SWITCHING	50	..Complementary FET's
18	.Bipolar transistor	51	INHIBITOR
19	..With Schottky device	52	EXCLUSIVE FUNCTION (E.G., EXCLUSIVE OR, ETC.)
20	...Complementary transistors		
21	SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY	53	.Half-adder or quarter-adder
22	.Input noise margin enhancement	54	.Exclusive NOR
23	..With field-effect transistor	55	.With field-effect transistor
24	...Complementary FET's	56	TRI-STATE (I.E., HIGH IMPEDANCE AS THIRD STATE)
25	...Depletion or enhancement		
26	.Output switching noise reduction	57	.With field-effect transistor
27	..With field-effect transistor	58	..Complementary FET's
28	...With clocking	59	THREE OR MORE ACTIVE LEVELS (E.G., TERNARY, QUATERNARY, ETC.)
29	.Pulse shaping (e.g., squaring, etc.)		
30	.Bus or line termination (e.g., clamping, impedance matching, etc.)	60	.With conversion (e.g., three level to two level, etc.)
31	.Signal level or switching threshold stabilization	61	INSULATED GATE CHARGE TRANSFER DEVICE
32	..Temperature compensation	62	INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT, ETC.)
33	..Bias or power supply level stabilization	63	.Logic level shifting (i.e., interface between devices of different logic families)
34	..With field-effect transistor	64	..Bi-CMOS
35	THRESHOLD (E.G., MAJORITY, MINORITY, OR WEIGHTED INPUTS, ETC.)	65	...TTL to/from CMOS
		66	...ECL to/from CMOS
		67	...ECL to/from TTL
36	.With field-effect transistor	68	..Field-effect transistor (e.g., JFET, MOSFET, etc.)

69	...ECL to/from GaAs FET (e.g., MESFET, etc.)	104	FUNCTION OF AND, OR, NAND, NOR, OR NOT
70	...TTL to/from MOS	105	.Decoding
71	...TTL to/from CMOS	106	..With field-effect transistor
72Using depletion or enhancement transistors	107	...Depletion or enhancement
73	...ECL to/from MOS	108	...CMOS
74	...ECL to/from TTL	109	.Bipolar and FET
75	..Bipolar transistor	110	..Bi-CMOS
76	...TTL to/from MOS	111	.Space discharge device (e.g., vacuum tube, etc.)
77	...ECL to/from MOS	112	.Field-effect transistor (e.g., JFET, etc.)
78	...ECL to/from TTL	113	..Pass transistor logic or transmission gate logic
79	..Integrated Injection Logic (I ² L)	114	..Wired logic (e.g., wired-OR, wired-AND, dotted logic, etc.)
80	..Supply voltage level shifting (i.e., interface between devices of a same logic family with different operating voltage levels)	115	..Source-coupled logic (e.g., current mode logic (CML), differential current switch logic (DCSL), etc.)
81	..CMOS	116	..Schottky-gate FET (i.e., MESFET)
82	..Current driving (e.g., fan in/out, off chip driving, etc.)	117	...Depletion or enhancement
83	..Field-effect transistor	118	...Diode transistor logic
84	...Bi-CMOS	119	..MOSFET (i.e., metal-oxide semiconductor field-effect transistor)
85Having plural output pull-up or pull-down transistors	120	...Depletion or enhancement
86	..Bus driving	121	...CMOS
87	..Having plural output pull-up or pull-down transistors	122	..Complementary FETs
88	..With capacitive or inductive bootstrapping	123	..With semiconductor diode or negative resistance device
89	..Bipolar transistor	124	.Bipolar transistor (e.g., RTL, DCTL, etc.)
90	..Bus driving	125	..Wired logic or open collector logic (e.g., wired-OR, wired-AND, dotted logic, etc.)
91	..Having plural output pull-up or pull-down transistors	126	..Emitter-coupled or emitter-follower logic
92	..With capacitive or inductive bootstrapping	127	...Current mode logic (CML)
93	CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR GATES	128	..Transistor-transistor logic (TTL)
94	..Metastable state prevention	129	...Complementary transistor logic (CTL)
95	..Field-effect transistor	130	..Diode-transistor logic (DTL)
96	..Two or more clocks (e.g., phase clocking, etc.)	131	...With metal semiconductor junction diode (e.g., Schottky barrier, etc.)
97	...MOSFET	132	..With negative resistance device (e.g., tunnel diode, thyristor, etc.)
98	..MOSFET	133	.Diode
99	HAVING LOGIC LEVELS CONVEYED BY SIGNAL FREQUENCY OR PHASE	134	..Negative resistance diode (e.g., tunnel, gunn, etc.)
100	INTEGRATED INJECTION LOGIC		
101	SIGNIFICANT INTEGRATED STRUCTURE, LAYOUT, OR LAYOUT INTERCONNECTIONS		
102	..Field-effect transistor		
103	..Complementary FET's		

135 .Negative resistance device
136 **MISCELLANEOUS**

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