

1	SPECIFIC SIGNAL DISCRIMINATING (E.G., COMPARING, SELECTING, ETC.) WITHOUT SUBSEQUENT CONTROL	39	.By frequency
		40	..Comparison between plural inputs
		41	..With synchronous detection
		42	..Fixed frequency reference signal
		43	..With logic or bistable circuit
		44	..With predetermined frequency selection
		45	..Including sampling or reference frequency
		46	..Including plural frequency detection
		47	..Frequency detection
		48	..With counting
		49	..With logic or bistable circuit
		50	.By amplitude
		51	..With sensing amplifier
		52	...Differential amplifier
		53	...Current mirror
		54	...Having feedback
		55Cross-coupled
		56With reference signal
		57	...With latching type element (e.g., flip-flop, etc.)
		58	..Maximum or minimum amplitude
		59	..Employing input compared to output
		60	..Employing input compared to reference derived therefrom
		61	..By diode-capacitor network
		62	...Maximum and minimum amplitude
		63	..Comparison between plural varying inputs
		64	...With logic or bistable circuit
		65	...Differential input
		66	...Current mirror
		67	...Having feedback
		68	...Input provides varying reference signal
		69	...With plural paths
		70With single output
		71	...Three or more inputs
		72	..Input signal compared to reference derived therefrom
		73	..Reference derived by feedback
		74	..Input signal compared to plural fixed references
		75	...Three or more
		76	...With logic or bistable circuit
		77	..Input signal compared to single fixed reference
2	.By phase		
3	..Comparison between plural inputs (e.g., phase angle indication, lead-lag discriminator, etc.)		
4	...With transducer		
5	...With input derived from feedback		
6	...With electron space discharge		
7	...With reference signal		
8With varying frequency		
9With sampling		
10Uniform pulse waveform		
11With transformer		
12	...With logic or bistable circuit		
13	.By shape		
14	..Slope		
15	...With direction (i.e., positive or negative)		
16	..Having feedback		
17	..With reference signal		
18	.By presence or absence pulse detection		
19	..Arbitration		
20	..Monitoring (e.g., failure detection, etc.)		
21	..With variable frequency source		
22	.By pulse noncoincidence		
23	.By pulse coincidence		
24	..Edge sensing		
25	..With uniform spacing		
26	..With pulse width detecting		
27	..With reference		
28	.By polarity		
29	..Selection of a particular polarity		
30	..Opposite polarity		
31	.By pulse width or spacing		
32	..With shock-excited circuit		
33	..With sampling		
34	..Narrow pulse elimination or suppression		
35	..Separating by duration or gap (e.g., duty cycle, etc.)		
36	..Selection of a particular pulse width		
37	..Comparison by threshold or reference		
38	..With plural paths		

78Reference level crossover detecting	114 115	..Of output rectangular waveform ...Frequency division
79Zero crossover	116	...Frequency multiplication
80Reference determined by threshold of single circuit element	117 118	.Frequency division ...Having discrete active device (e.g., transistor, triode, etc.)
81With transistor	119	...Frequency multiplication (e.g., harmonic generation, etc.)
82Plural sources of input signal	120With plural outputs
83Temperature compensation	121Selective
84With bridge circuit	122Doubling
85Inverting input or output	123	...With particular tube or distributed parameter element
86With transformer	124	.By periodic switching (e.g., chopper, etc.)
87Having feedback	125	.Generating parabolic or hyperbolic output
88With source as reference	126	.Generating staircase output
89With differential amplifier	127	..With differential amplifier
90	..Comparison between two characteristics of an input signal	128	..With rectifying element
91	..Including details of sampling or holding	129	.Generating sinusoidal output
92	...With bridge circuit	130	.Generating trapezoidal output
93	...With reference source	131	.Generating sawtooth or triangular output
94	...Sample and hold	132	..With current source or current mirror
95Having feedback	133	..With distortion control (e.g., linearization, etc.)
96With differential amplifier	134	..With slope or duration control
97	..With logic or bistable circuit	135	..Having digital element
98	.By separating composite signal	136	..Having particular delay or sync
99	.Having selection between plural continuous waveforms	137	..Having feedback
100	SIGNAL CONVERTING, SHAPING, OR GENERATING	138	..Having temperature compensation
101	.Converting input current or voltage to output frequency	139	..Having inductive load
102	.Converting input frequency to output current or voltage	140	..With amplitude control
103	.Converting input voltage to output current or vice versa	141	.Synchronizing
104	.Converting, per se, of an AC input to corresponding DC at an unloaded output	142	..Reset (e.g., initializing, starting, stopping, etc.)
105	.Synthesizer	143	...Responsive to power supply
106	..Having stored waveform data (e.g., in ROM, etc.)	144	..Using multiple clocks
107	..Having digital device (e.g., logic gate, flip-flop, etc.)	145	...Having different frequencies
108	.Current driver	146	...With feedback
109	..Having semiconductive load	147Phase lock loop
110	..Having inductive load (e.g., coil, etc.)	148With charge pump
111	..Having capacitive load	149With variable delay means
112	...Push-pull	150With digital element
113	.Frequency or repetition rate conversion or control	151	...With counter
		152	...With choice between multiple delayed clocks
		153	...With delay means
		154	..With feedforward
		155	..With feedback

156Phase lock loop	183	.Delay line or capacitor storage
157With charge pump		element charges or discharges
158With variable delay means		through a tube to form pulse
159With digital element	184	.Rectangular (e.g., clock, etc.)
160	..With counter		or pulse waveform generating
161	..With delay means		by conversion from input AC
162	..Having reference source		(e.g., sine, etc.) wave
163	...By phase	185	.Particular stable state circuit
164	.Generating rectangular (e.g., clock, etc.) or pulse waveform having random characteristic (e.g., random width, etc.)	186	(e.g., tristable, etc.)
165	.Regenerating or restoring rectangular (e.g., clock, etc.) or pulse waveform	187	.Superconductive (e.g., cryogenic, etc.)
166	..Having digital device (e.g., logic gate, flip-flop, etc.)	188	.External effect device (e.g., light, heat, magnetic, or mechanical force sensitive devices, etc.)
167	..Having network providing particular mathematical function (e.g., integrator, etc.)	189	.Minority carrier storage effect
168	..Having inductive device (e.g., transformer, etc.)	190	...Storage diode (e.g., step recovery, etc.)
169	..Having negative resistance device (e.g., tunnel diode, etc.)	191	..With transformer or saturable core device
170	.Slope control of leading or trailing edge of rectangular (e.g., clock, etc.) or pulse waveform	192	...Blocking oscillator
171	.Output pulses having opposite polarities	193	..Negative resistance transistor (e.g., unijunction, etc.)
172	.Rectangular (e.g., clock, etc.) or pulse waveform width control	194	...Four or more layer device, (e.g., trigistor, etc.)
173	..Pulse narrowing	195	..Zener or capacitive diode
174	..Pulse broadening		..Negative resistance diode
175	..Duty cycle control		having "N"-shape characteristic on I-V plot
176	..Having digital device (e.g., logic gate, flip-flop, etc.)	197	(e.g., tunnel diode, backward diode, etc.)
177	..Having inductive device (e.g., transformer, etc.)	198	.Negative resistance diode having "S"-shape characteristic on I-V plot
178	.Rectangular (e.g., clock, etc.) or pulse waveform amplitude control	199	(e.g., four or more layer semiconductor device, etc.)
179	..Gain	200	.Convertible circuit (e.g., bistable to monostable, D-type to T-type, etc.)
180	..Limiting, clipping, or clamping	201	..Initializing, resetting, or protecting a steady state condition
181	.Electromagnetic pulse forming	202	..Circuit having only two stable states (i.e., bistable)
182	.Delay line or capacitor storage element charged or discharged through or by a relaxation oscillator type circuit to form pulse	203	...Dynamic bistable
		204Complementary clock inputs
		205Master-slave bistable latch
		Including field-effect transistor
		Including multi-emitter or multi-collector bipolar transistor
			...Using hysteresis (e.g., Schmitt trigger, etc.)

206Including field-effect transistor	239	...Non-overlapping multiple outputs
207Including diverse solid state devices (e.g., FET/bipolar, etc.)	240	...Maintaining invariant amplitude
208Including field-effect transistor	241	...With counter or shift register
209Including enhancement and depletion devices	242Having multiple outputs
210CMOS	243	...With feedback
211With clock input	244With phase comparator or detector
212With clock input	245Having multiple outputs
213Plural independent clock inputs (i.e., non complementary)	246With differential amplifier
214Complementary transistors	247Having multiple outputs
215	...Having at least two cross-coupling paths	248	...With adder
216JK type input	249Having multiple outputs
217RS or RST type input	250	...With active time delay element
218D type input	251Having multiple outputs
219Particular device at input, output, or in cross-coupling path	252	...With passive time delay element
220With diode	253Having multiple outputs
221Parallel RC network in cross-coupling path	254	..Quadrature related (i.e., 90 degrees)
222Resistor in cross-coupling path	255	...90 degrees between input and output
223	...Plural transistors of same conductivity type	256	.Phase inversion (i.e., 180 degrees between input and output)
224	...With single semiconductor device	257Multiple outputs
225	...With logic element (e.g., NOR gate, etc.)	258	..Multiple outputs
226	...With single electron tube	259	...Non-overlapping
227	..Monostable	260	..Producing AC power control
228	...Having cross-coupled paths	261	.Having specific delay in producing output waveform
229	...Having differential circuitry	262	..Including significant compensation (e.g., temperature compensated delay, etc.)
230	...With external feedback (i.e., output to input)	263	.Delay interval set by rising or falling edge
231	.Phase shift by less than period of input	264	..Having specific active circuit element or structure (e.g., FET, complementary transistors, etc.)
232	..Dependent on frequency	265With counter
233	..Correction to specific phase shift	266Differential amplifier
234	...Dependent on variable controlled phase shifts	267Electron tube
235	...Dependent on multiple fixed phase shifts	268	...Having specific passive circuit element or structure (e.g., RLC circuit, etc.)
236	...By phase comparator or detector	269	..Multiple outputs with plurality of delay intervals
237	..Variable or adjustable	270	...Variable or adjustable
238	...Quadrature related (i.e., 90 degrees)	271Including delay line or charge transfer device

272	...Having specific active circuit element or structure (e.g., FET, complementary transistors, etc.)	305 306 307	..With gas tube .Amplitude control .Baseline or DC offset correction
273With counter	308	..Variable attenuator
274Differential amplifier	309	..By limiting, clipping, or clamping
275Electron tube	Transient or signal noise reduction
276	..Single output with variable or selectable delay	310By filtering
277	...Including delay line or charge transfer device	311 312By feedback limiting-clamping
278	...Having specific active circuit element or structure (e.g., complementary transistors, etc.)	313 314Using 3 or more terminal type nonlinear devices onlyUsing diode type nonlinear devices only
279With counter	315Providing constant input/output amplitude level ratio
280Differential amplifier	By feedback control
281Field-effect transistor	316Distortion compensation
282Electron tube	317	...In input or output circuit
283	...Having specific passive circuit element or structure (e.g., RLC circuit, etc.)	318 319 320For interstage couplingUsing diode
284	..Including delay line or charge transfer device	321Clamping of output to voltage level
285	..Having specific active circuit element or structure (e.g., complementary transistors, etc.)	322 323 324Of output current ...Feedback .By using diverse-type nonlinear devices
286	...With counter	325	...Using only diode active elements
287	...Differential amplifier	Avalanche or negative resistance device (e.g., zener diode, tunnel diode, etc.)
288	...Field-effect transistor	326	...Using only transistor active elements
289	...Electron tube	Field-effect type device
290	..Having specific passive circuit element or structure (e.g., RLC circuit, etc.)	327	...With tuned circuit
291	.Clock or pulse waveform generating	328	...With rectifier or nonlinear impedance
292	..Clock fault compensation or redundant clocks	329 330	..Maintaining constant level output
293	..With plural paths in network	331	...With feedback
294	...With common output		..Interstage coupling (e.g., level shift, etc.)
295	..Plural outputs	332	SPECIFIC INPUT TO OUTPUT FUNCTION
296	...Plural clock outputs with multiple inputs	333	.By differentiating
297	...Clock bus	334	.By integrating
298	..Single clock output with multiple inputs	335	..Having switched capacitance
299	..Single clock output with single clock input or data input	336 337	..With thermionic tube
300	..With saturable inductance	338	..With summing or counting
301	..With electron beam type tube	339	..Single vacuum tube
302	..With storage diode	340	..With compensation
303	..With rectifier	341	..With transducer
304	..With inductive device (e.g., transformer, etc.)	342 343	..With rectifier circuit

344	..Including RC circuit	387	..Control signal derived from or responsive to input signal
345	..Having feedback	388	...Additional external control signal
346	.Exponential	389	..Insulated gate FET (e.g., MOSFET, etc.)
347	..Square root	390	...With capacitive bootstrapping
348	...RMS	391	...Complementary metal-oxide semiconductor (CMOS)
349	..Square function	392	.Delay controlled switch (e.g., fixed, single time of delay control, etc.)
350	.Logarithmic	393	..With variable or multiple adjustable time of delay control (e.g., variable charge-discharge, on-delay/ off-delay control, etc.)
351	..With cascade network	394	..With field-effect device
352	..With summing	395	...Propagation through plural delay devices or paths
353	..With vacuum tube	396	...With plural switching elements (e.g., sequential, etc.)
354	.Absolute value	397	...Including negative resistance device in delay circuit (e.g., unijunction transistor, etc.)
355	.Combining of plural signals	398	..For predetermined time period
356	..Product	399	..With field-effect device
357	...Quadrant	400	...Propagation through plural delay devices or paths
358	...Having feedback	401	..With plural switching elements (e.g., sequential, etc.)
359	...Differential amplifier	402	..Including negative resistance device in delay circuit (e.g., unijunction transistor, etc.)
360	..Quotient	403	.Parallel controlled paths
361	..Summing	404	..Field-effect transistor
362	.With compensation	405	..Bipolar transistor
363	.Having feedback	406	..Electron tube
364	.With vacuum tube	407	.Converging with plural inputs and single output
365	GATING (I.E., SWITCHING INPUT TO OUTPUT)	408	..Field-effect transistor
366	.Superconductive (e.g., cryogenic, etc.) device	409	...Push-pull circuit
367	..Josephson junction	410With complementary transistor devices
368	..Critical current control	411	..Bipolar transistor
369	..External control (e.g., piezoelectric, light, etc.)	412	...Push-pull circuit
370	...Magnetic field control	413With complementary transistor devices
371	...Temperature control	414	..Electron tube
372	..Inductive effect	415	.Diverging with single input and plural outputs
373	..Layout	416	..Field-effect transistor
374	.Accelerating switching	417	..Bipolar transistor
375	..Saturation prevention	418	..Electron tube
376	..Turn-on		
377	..Turn-off		
378	.Compensation for variations in external physical values (e.g., temperature, etc.)		
379	.Signal transmission integrity or spurious noise override		
380	..Preventing quick rise gating current (i.e., di/dt)		
381	..Preventing quick rise gating voltage (i.e., dv/dt)		
382	..Parasitic prevention or compensation (e.g., parasitic capacitance, etc.)		
383	..Ensuring fully conducting state		
384	..Switch noise signal		
385	...Contact bounce from mechanical switch		
386With clock input		

419	.Utilizing three or more electrode solid-state device	456Plural
420	..Breakdown characteristic (e.g., punch-through, tunneling, etc.)	457Combined with diac
420	..Breakdown characteristic (e.g., punch-through, tunneling, etc.)	458Combined with diverse four or more layer device
421	...Zener	459With bipolar transistor
422	...Avalanche	460Plural SCRs
423	...Bridge circuit	461Inverse parallel connection
424	...Field-effect transistor	462With bipolar transistor
425	..Bilateral transistor	463With bipolar transistor
426	...Plural	464Having plural four or more layer devices
427	..Field-effect transistor	465DC supply
428	...With silicon controlled rectifier (SCR)	466PUT (i.e., programmable unijunction transistor)
429	...Four or more electrode solid-state device	467Four electrodes
430	...JFET (i.e., junction field-effect transistor)	468SCR and unijunction transistor
431MESFET (i.e., metal semiconductor field-effect transistor)	469Triac
432	...With bipolar transistor	470Plural devices
433Bi-CMOS	471Series anode-cathode connection
434Insulated gate FET (e.g., MOSFET, etc.)	472Plural paths
435GaAs	473Parallel connection
436Plural devices in series	474With bipolar transistor
437Complementary metal-oxide semiconductor (CMOS)	475SCR and bipolar transistor
438	..Four or more layer device (e.g., thyristor, etc.)	476Triac
439	...Bipolar transistor circuit configuring SCR device	477	..Unijunction transistor (UJT)
440	...GTO (i.e., gate turnoff)	478	..Bipolar transistor
441Plural or combined with other four or more layer device	479	...Special four or more electrode device (e.g., multiple bases, three electrode bipolar with FET gate, etc.)
442Separate ON and OFF control circuit	480Multiple emitter transistor
443Transformer or inductor in control circuit	481Multiple collector transistor
444	...Complex wave supply	482	...Plural
445Silicon controlled rectifier (SCR)	483Darlington connection
446Triac	484Opposite conductively (i.e., complementary)
447	...AC supply	485Control circuit in cascade
448Device in bridge	486Control circuit in totem pole
449PUT (i.e., programmable unijunction transistor)	487Control circuit in cascade
450Four electrodes	488Control circuit in totem pole
451Zero point switching	489Control circuit with common emitter
452With triac	490With current mirror
453Silicon controlled rectifier (SCR)	491With emitter follower
454With unijunction transistor	492Control circuit with common collector
455Triac	493	.Utilizing two electrode solid-state device
		494	..Bridge circuit
		495	...Combined with diverse device in at least one arm

496	...Plural	534	.Having particular substrate biasing
497	...Active element in diagonal arm		...Having stabilized bias or power supply level
498	..Negative resistance	535Charge pump details
499	..."N"-shape curve on I-V plot (e.g., tunnel diode type, etc.)	536With field-effect transistor
500	..."S"-shape curve on I-V plot (e.g., pnnp diode type, etc.)	537	..Stabilized (e.g., compensated, regulated, maintained, etc.)
501Hyperconductive diode	538	...Using bandgap
502	..Breakdown characteristic (e.g., zener diode, etc.)	539	...With voltage source regulating
503	..PIN diode	540With field-effect transistor
504	..PN junction diode	541	...With diverse type transistor devices
505	...Inverse parallel connection	542	...Using field-effect transistor
506	.Three or more electrode electron tube	543	..Power conservation or pulse type
507	.Two electrode electron tube	544	..Including signal protection or bias preservation
508	..Bridge circuit	545	...With field-effect transistor
509	EXTERNAL EFFECT	546	..With selectively or alternately DC or AC input
510	.Magnetic	547	..With oscillator or interrupter
511	..Utilizing Hall effect	548	..With hum or interaction prevention
512	.Temperature	549	..With particular filament heating circuit
513	..With compensation for temperature fluctuations	550	.Unwanted signal suppression
514	.Light	551	..Active filter
515	..Elements forming an array	552	...Adjustable
516	.Utilizing conversion of mechanical variations into electrical variations (e.g., vibration sensitive, etc.)	553Switched capacitor filter
517	.Responsive to proximity or touch	554Selective type signal filtering (e.g., from low pass to high pass, etc.)
518	WITH PARTICULAR CONTROL	555	...Notch or bandreject
519	.Plurality of load devices	556	...Bandpass
520	.Plural active components included in a controlling circuit	557	...Lowpass
521	..Connected in inverse parallel	558	...Highpass
522	..Gaseous tube	559	.Nonlinear amplifying circuit
523	.Gaseous tube	560	..With operational amplifier
524	SPECIFIC IDENTIFIABLE DEVICE, CIRCUIT, OR SYSTEM	561	...With field-effect transistor
525	.Fusible link or intentional destruct circuit	562	..With differential amplifier
526	.Redundant	563	.Integrated structure
527	.Superconductive (e.g., cryogenic, etc.) device	564	..With specific layout or layout interconnections
528	..Josephson junction	565	...Having field-effect transistor device
529	..Impact ionization	566	.Thin film
530	.With specific source of supply or bias voltage	567	.Negative resistance type
531	..Fluctuating or AC source with rectifier or filter	568	..Unijunction transistor
532	...With particular filter circuit	569	..Having "N"-shape curve on I-V plot (e.g., tunnel diode type, etc.)
533	...With battery connected across rectifier	570	..Having "S"-shape curve on I-V plot (e.g., pnnp diode type, etc.)

572 ..Secondary emissive type
573 ...Electron multiplier type
574 .Utilizing a three or more
 electrode solid-state device
575 ..Darlington connection
576 ..Complementary transistors
577 ..Multiple emitter transistor
578 ..Multiple collector transistor
579 ..Minority carrier storage
580 ..Transistor breakdown device
 (e.g., avalanche, zener, punch
 through, etc.)
581 ..Field-effect transistor
582 ..Four or more layer device
 (e.g., silicon-controlled
 rectifier, etc.)
583 .Utilizing two electrode solid-
 state device
584 ..Breakdown diode (e.g., zener
 diode, avalanche diode, etc.)
585 ..Minority carrier storage diode
 (e.g., enhancement diode,
 etc.)
586 ..Capacitive diode
587 ..Bridge circuit
588 .With bridge circuit
589 .With bootstrap circuit
590 .With particular feedback
591 .Tube performs plural functions
592 .With oscillation prevention
593 .With distributed parameter
 circuit
594 .With particular coupling or
 decoupling
595 .With particular connecting
596 .Including oscillatory or shock-
 excited circuit
597 .With particular grid control
598 .With particular tube structure
599 ..Vacuum tube type
600 ...Beam tube structure
601 ..Gas tube
602 ...With particular electrode
 arrangement
603 **MISCELLANEOUS**

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